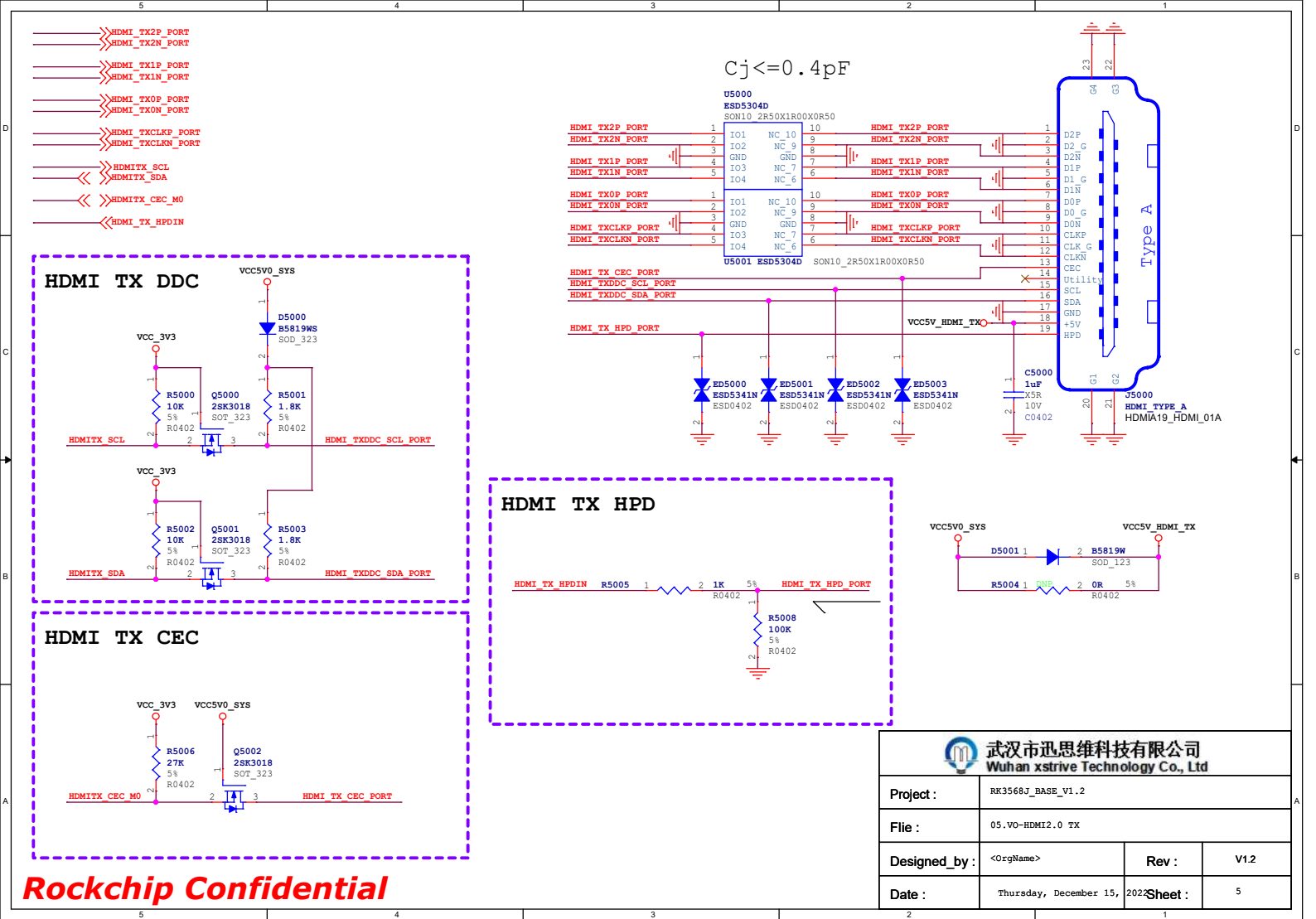
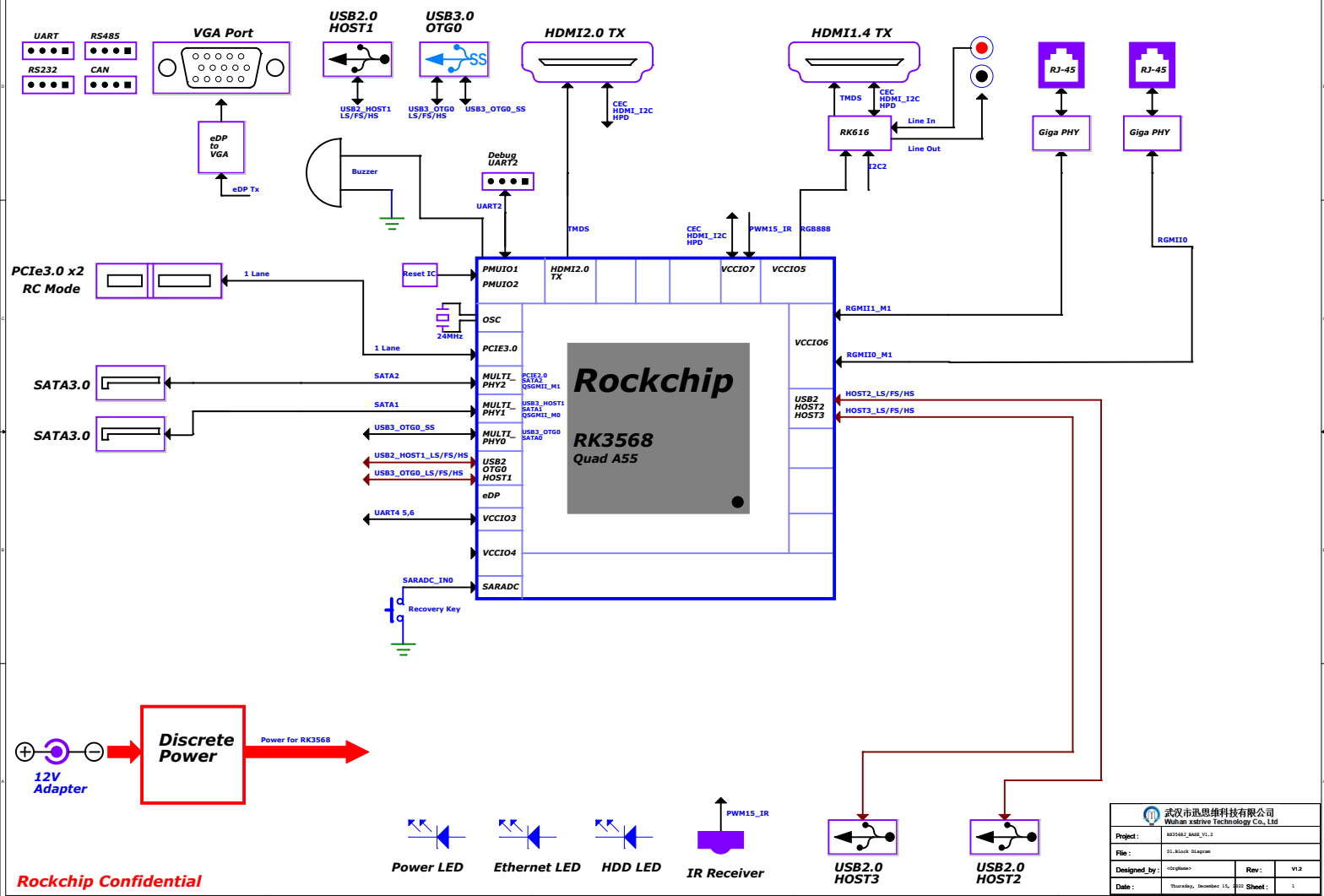


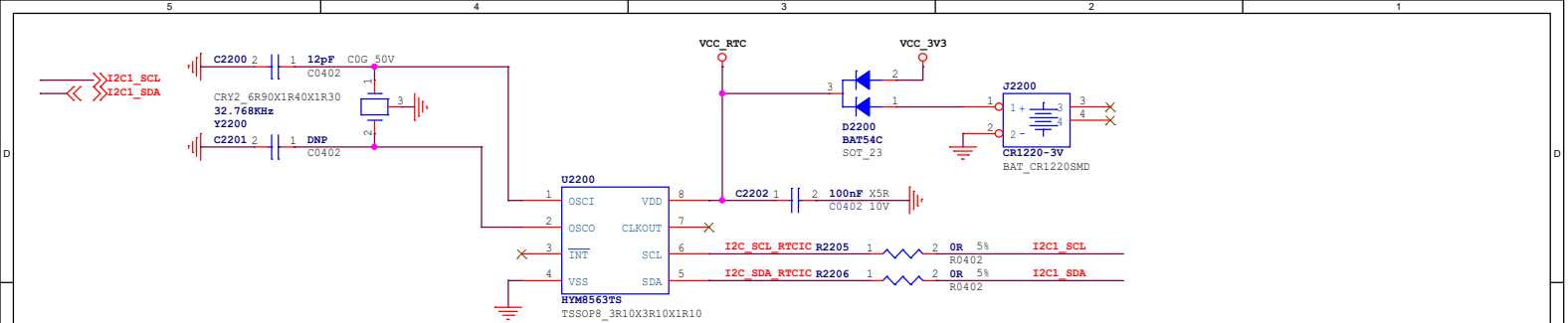


RK3568 DEMO Block Diagram



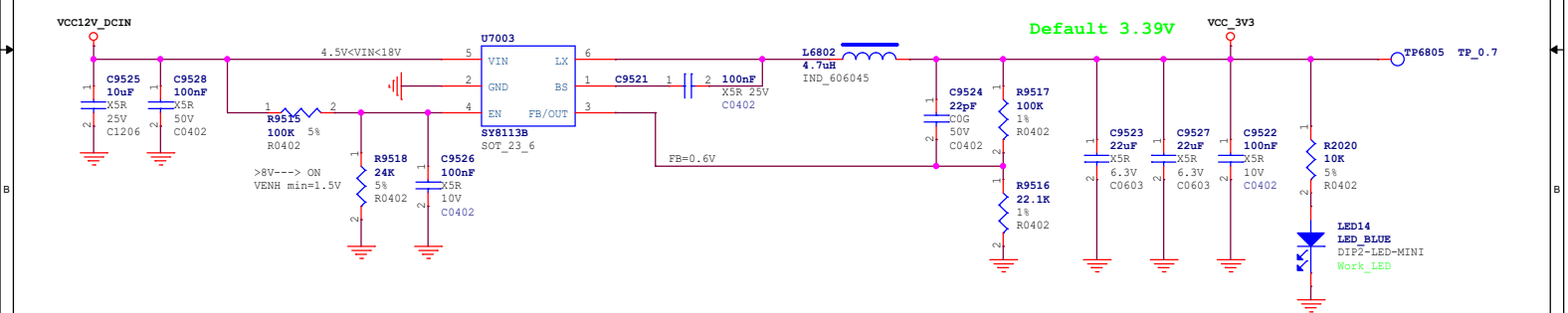
武汉市迅思维科技有限公司 Wuhan Xunsiwei Technology Co., Ltd.	
Project:	RS35682_M00_V1.0
File:	01.Block Diagram
Designed by:	Guoqiang
Date:	Thursday, December 15, 2022
Rev:	V1.2
Sheet:	1

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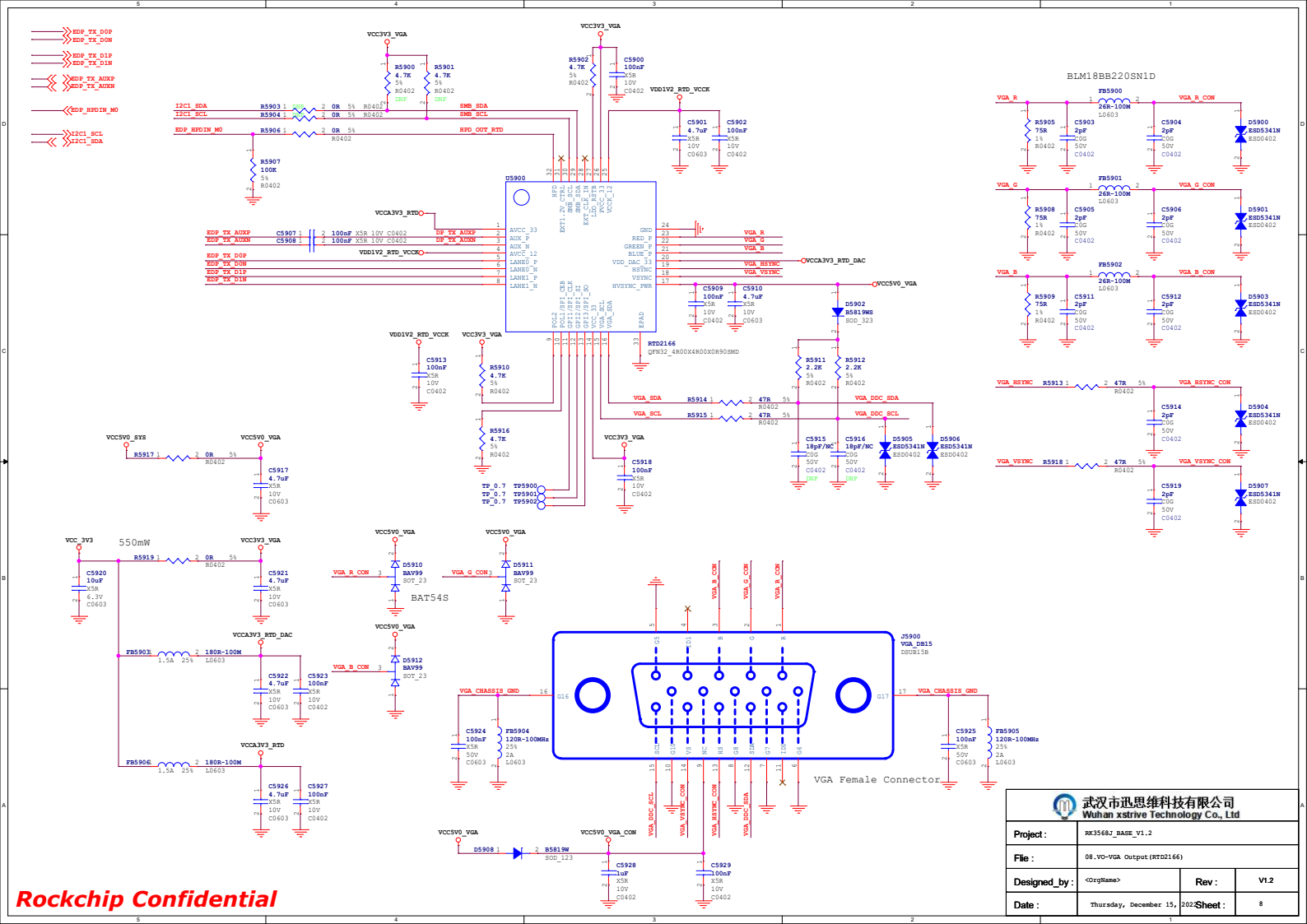


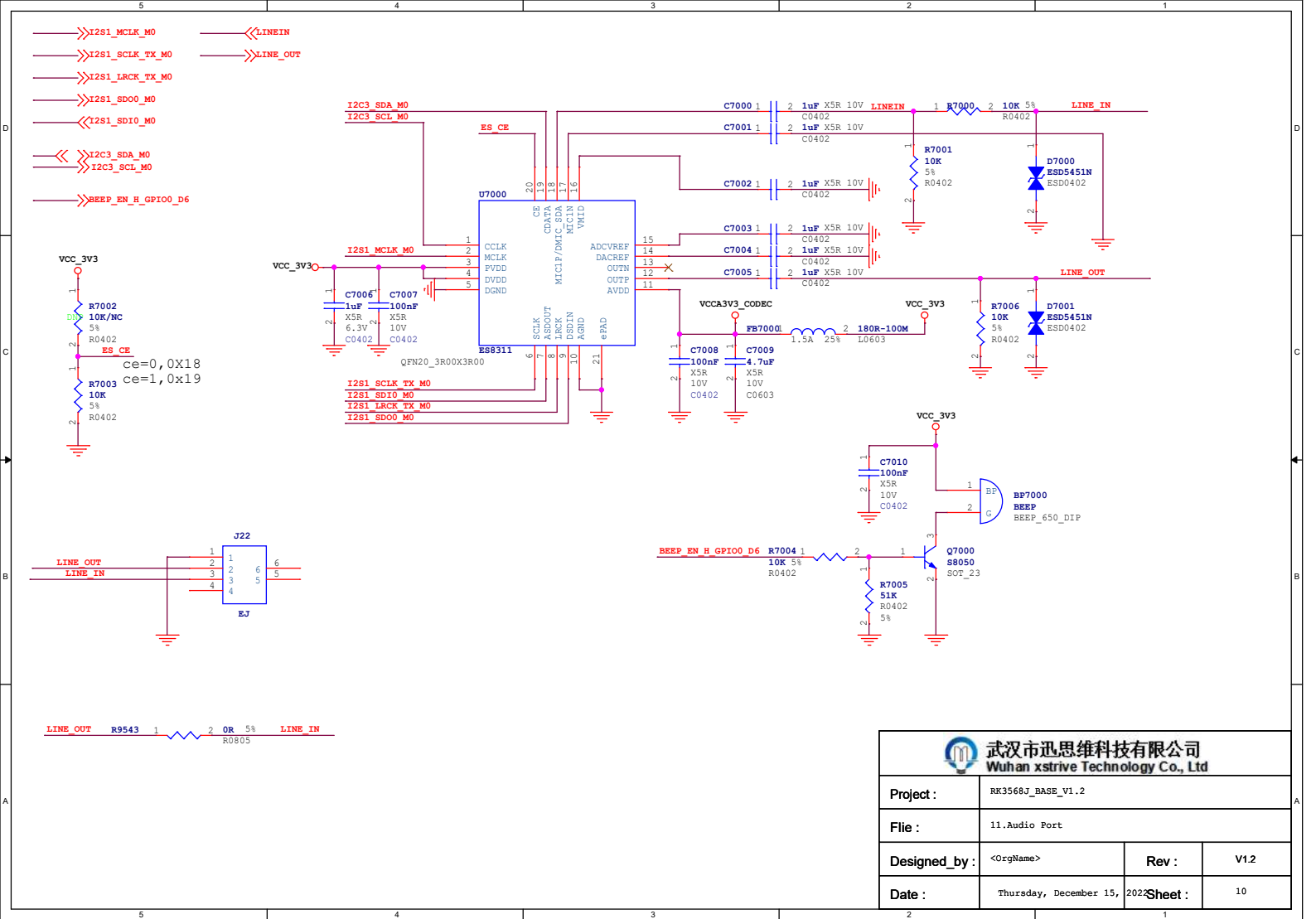
RTC IC

Address:Read A3H,Write A2H



 武汉市迅思维科技有限公司 Wuhan xstrive Technology Co., Ltd			
Project :	RK3568J_BASE_V1.2		
File :	03.Power_other		
Designed by :	<OrgName>	Rev :	V1.2
Date :	Wednesday, December 28, 2023	Sheet :	3

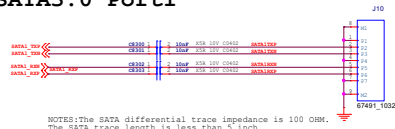




 武汉市迅思维科技有限公司 Wuhan xstrive Technology Co., Ltd			
Project :	RK3568J_BASE_V1.2		
File :	11.Audio Port		
Designed by :	<OrgName>	Rev :	V1.2
Date :	Thursday, December 15, 2022	Sheet :	10

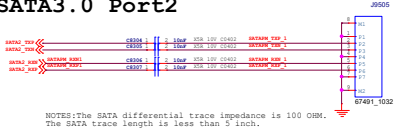
-----X-----SATA1_100
-----X-----SATA1_100
-----X-----SATA1_100
-----X-----SATA1_100
-----X-----SATA1_100
-----X-----SATA1_100
-----X-----SATA1_100
-----X-----SATA1_100

SATA3.0 Port1



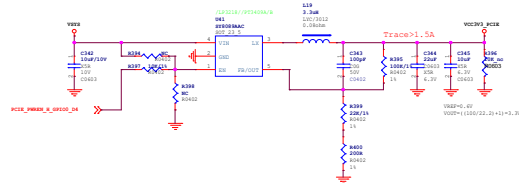
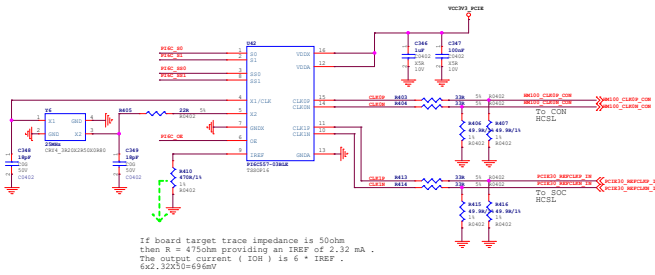
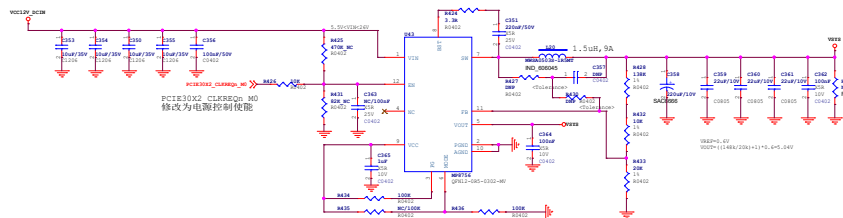
NOTES:The SATA differential trace impedance is 100 OHM.
The SATA trace length is less than 5 inch.

SATA3.0 Port2

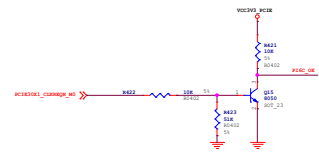


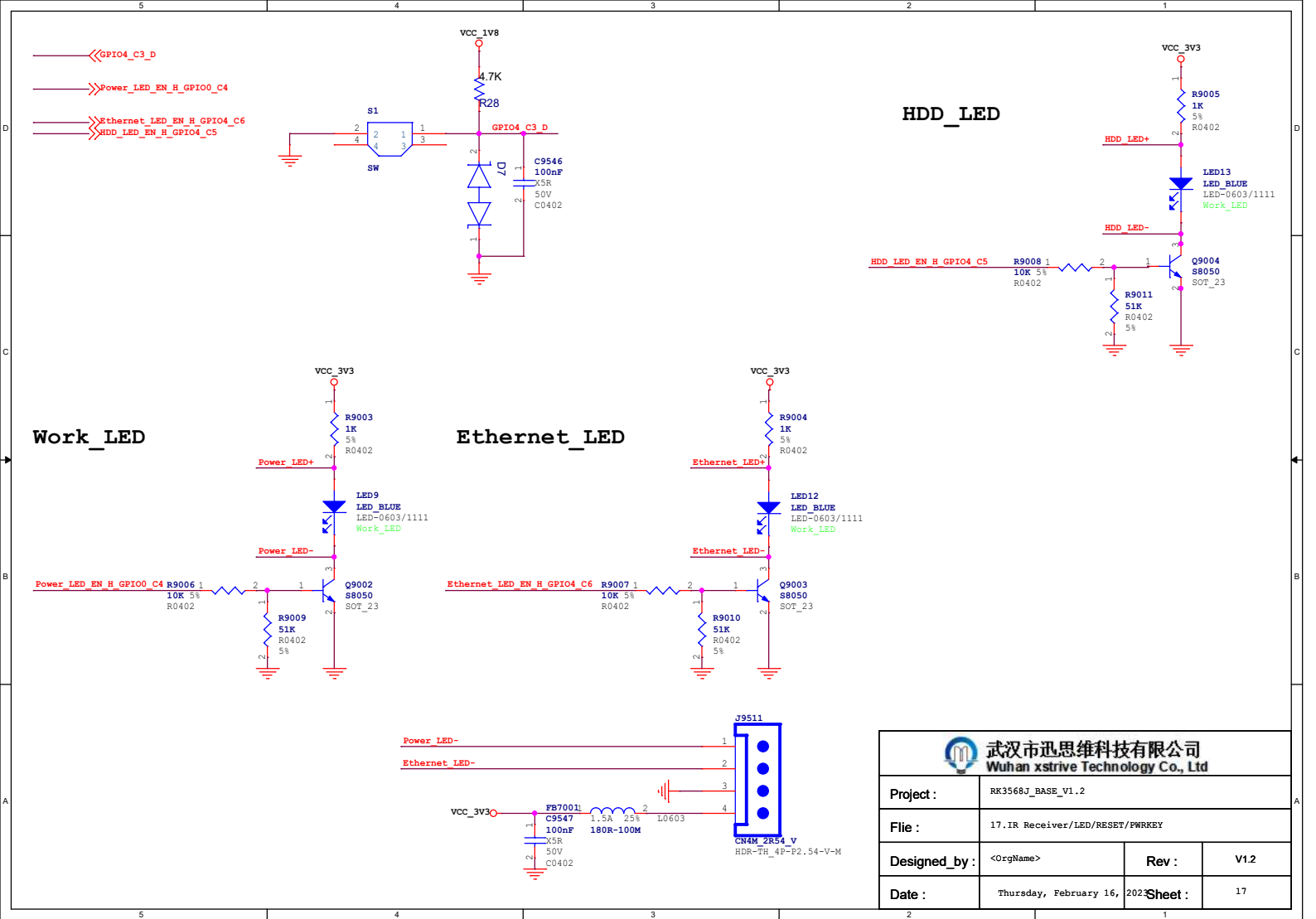
NOTES:The SATA differential trace impedance is 100 OHM.
The SATA trace length is less than 5 inch.

 武汉市迅思维科技有限公司 Wuhan Xunrui Technology Co., Ltd.			
Project :	WUTS002_M002_V1.0		
File :	12-0020-00203-0		
Designed_by :	ChenHao	Rev :	V1.0
Date :	Thursday, December 15, 2011	Sheet :	12

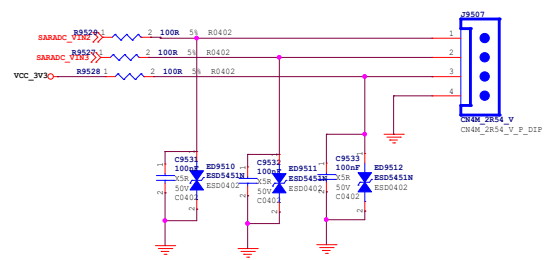
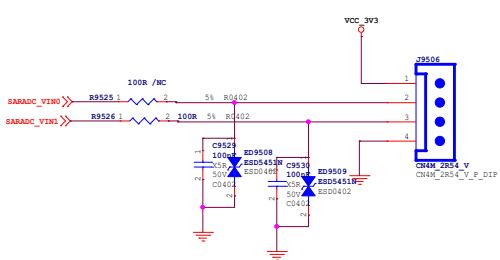
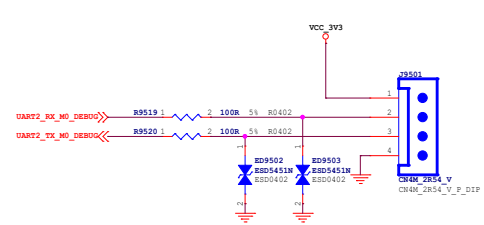


P16C_S1	P16C_S0	Out Freq
0	1	100MHz
P16C_S1	P16C_S0	Spread %
0	0	No Spread
0	1	-0.5
1	0	-1.0
1	1	No Spread



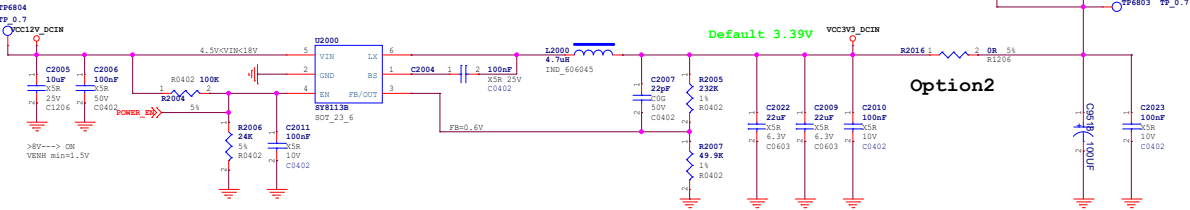


<<UART5_RX_NO
>>UART5_TX_NO
<<UART6_RX_NO
>>UART6_TX_NO
<<UART4_RX_NO
>>UART4_TX_NO

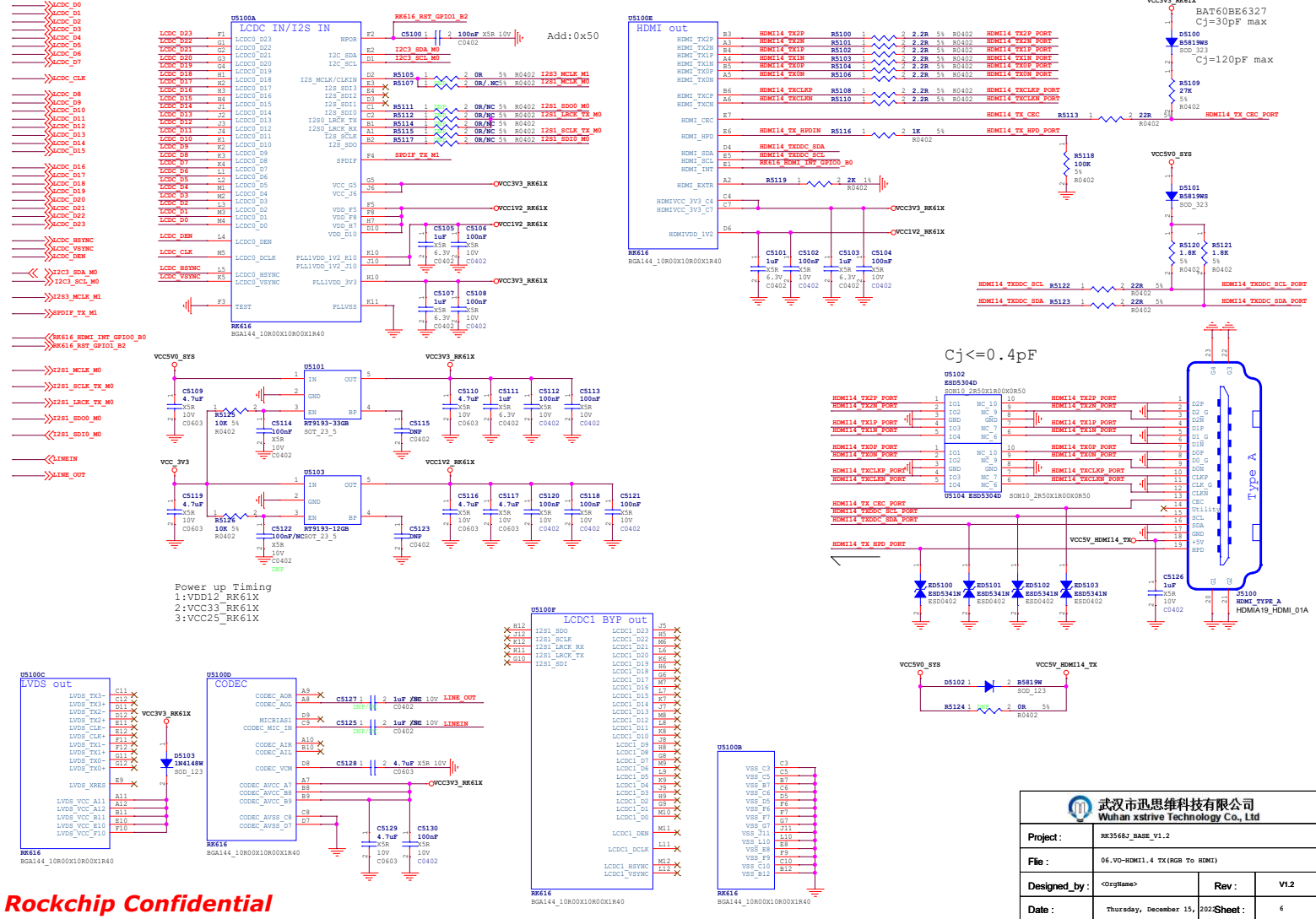


 武汉市迅思维科技有限公司 Wuhan xstrive Technology Co., Ltd			
Project :	RD35687_BA08_V1.2		
File :	15_UART7/RS485/RS232		
Designed_by :	<OrgName>	Rev :	V1.2
Date :	Thursday, February 16, 2024	Sheet :	15

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[illegible]

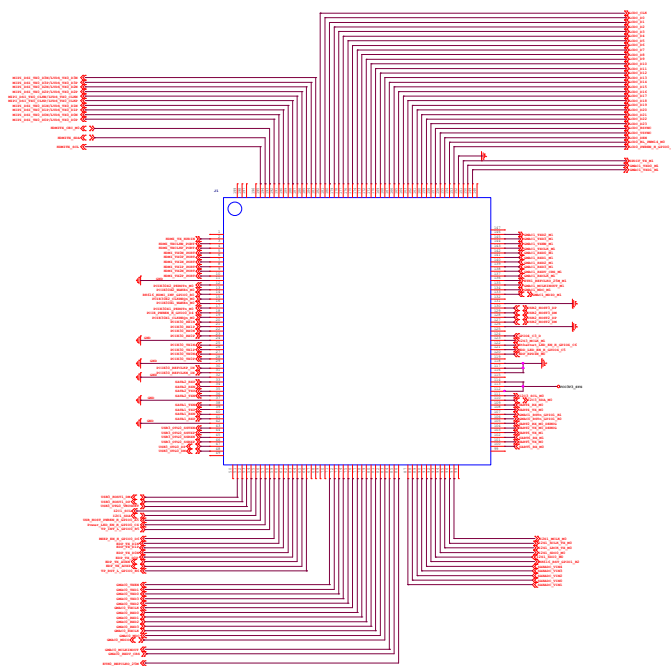
 武汉市迅思维科技有限公司 Wuhan xstrive Technology Co., Ltd			
Project :	RK3568J_BASE_V1.2		
File :	02.Power		
Designed by :	<CrgName>	Rev :	V1.2
Date :	Monday, February 13, 2023	Sheet :	2



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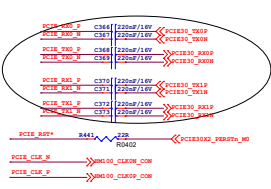
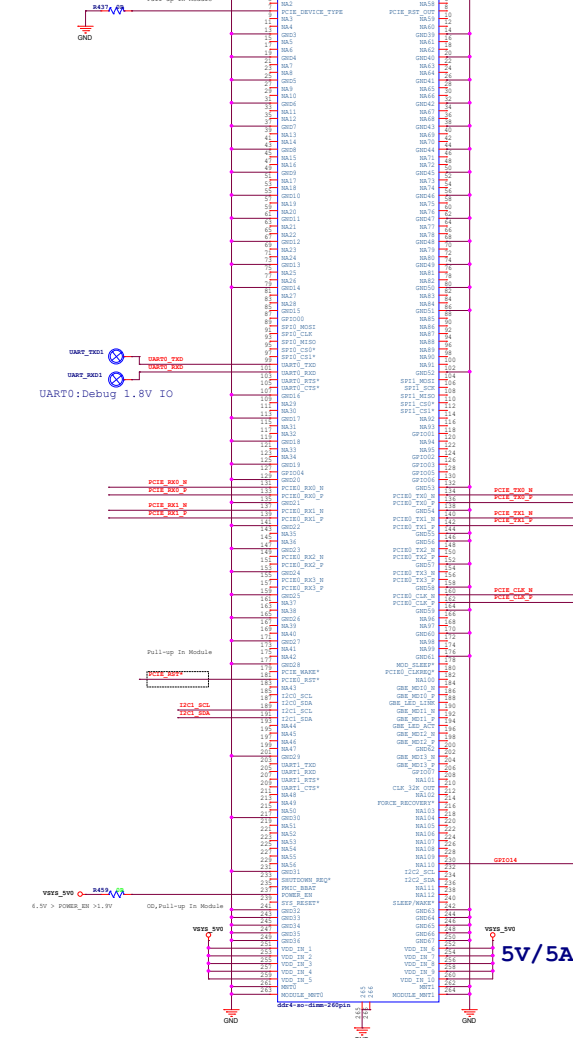
 武汉市迅思维科技有限公司 Wuhan xstrive Technology Co., Ltd			
Project:	RK3568_BASE_V1.2		
File:	06-VO-HDMI1.4 TX(8G to HDMI)		
Designed_by:	<GrgName>	Rev:	V1.2
Date:	Thursday, December 15, 2022	Sheet:	6



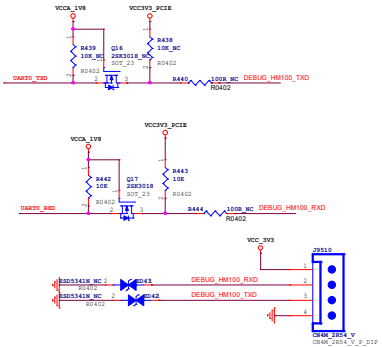
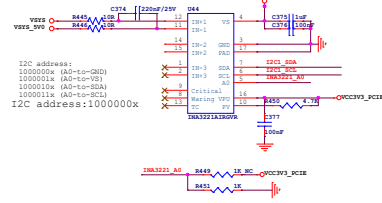


 武汉市机思科技有限公司 Wuhan Jishi Technology Co., Ltd.			
Project: WJSHI-2024-01			
File: WJSHI-2024-01-01			
Designed by:	WJSHI	Rev:	01.0
Date:	2024-01-01	Sheet:	01

SO-DIMM



MON



FAN

风扇口



武汉市迅思维科技有限公司 Wuhan xstirve Technology Co., Ltd			
Project :	W35682_BAS_V1.2		
File :	14.PCIEB000		
Designed_by :	<chengwen>	Rev :	V1.2
Date :	Thursday, December 15, 2022	Sheet :	14



RS232

CAN

CAN

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